

Remarks on Recognizability of Four-Dimensional Topological Components

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Abstract

The study of four-dimensional automata as the computational model of four-dimensional pattern processing has been meaningful. However, it is conjectured that the three-dimensional pattern processing has its own difficulties not arising in two- or three-dimensional case. One of these difficulties occurs in recognizing topological properties of four-dimensional patterns because the four-dimensional neighborhood is more complicated than two- or three-dimensional case. Generally speaking, a property or relationship is topological only if it is preserved when an arbitrary 'rubber-sheet' distortion is applied to the pictures. For example, adjacency and connectedness are topological; area, elongatedness, convexity, straightness, etc. are not. In recent years, there have been many interesting papers on digital topological properties. For example, an interlocking component was defined as a new topological property in multi-dimensional digital pictures, and it was proved that no one marker automaton can recognize interlocking components in a three-dimensional digital picture. In this paper, we deal with recognizability of topological components by four-dimensional Turing machines, and investigate some properties.

KeyWords : digital geometry, interlocking component, one marker automaton, three-dimensional automaton, topological component, Turing machine.

1 Introduction

Digital geometry has played an important role in computer image analysis and recognition[3]. In particular, there is a well-developed theory of *topological* properties such as *connectedness* and *holes* for two-dimensional arrays[4]. On the other hand, three-dimensional information processing has also become of increasing interest with the rapid growth of computed tomography, robotics, and so on. Thus it has become desirable to study the geometrical properties such as *interlocking components* and *cavities* for

three-dimensional arrays[2,5]. In[2], interlocking components was proposed as a new topological property of three-dimensional digital pictures: Let S_1 and S_2 be two subsets of the same three-dimensional digital picture. S_1 and S_2 are said to be interlocked when they satisfy the following conditions:

- (1) S_1 and S_2 are *toruses*,
- (2) S_1 goes through a *hole* of S_2 ,
- (3) S_2 goes through a *hole* of S_1 .

By the way, the question of whether processing four-dimensional digital pattern is much difficult than three-dimensional ones is of great interest from the theoretical and practical standpoints both. In recent years, due to the advances in many application areas such as computer animation, motion image processing, and so both, the study of four-dimensional pattern processing has been of crucial importance. Thus, it is very interesting to deal with the geometrical properties such as interlocking components and cavities in a four-dimensional digital picture.

The interlocking of S_1 and S_2 in a four-dimensional tape is illustrated in Fig.1. This relation may be considered as a chainlike connectivity.

It is proved that no one marker automaton can recognize interlocking components in a three-dimensional digital picture in [2]. In this paper, we investigate recognizability of topological properties such as interlocking components by three-dimensional Turing machines.

2 Preliminaries

Definition 2.1. Let Σ be a finite set of symbols. A *four-dimensional tape* over Σ is a four-dimensional array of elements of Σ . The set of all four-dimensional tapes over Σ is denoted by $\Sigma^{(4)}$. Given a tape $x \in \Sigma^{(4)}$, for each $j(1 \leq j \leq 4)$, we let $l_j(x)$ be the length of x along the j^{th} axis. When $1 \leq i_j \leq l_j(x)$ for each $j(1 \leq j \leq 4)$, let $x(i_1, i_2, i_3, i_4)$ denote the symbol in x with coordinates

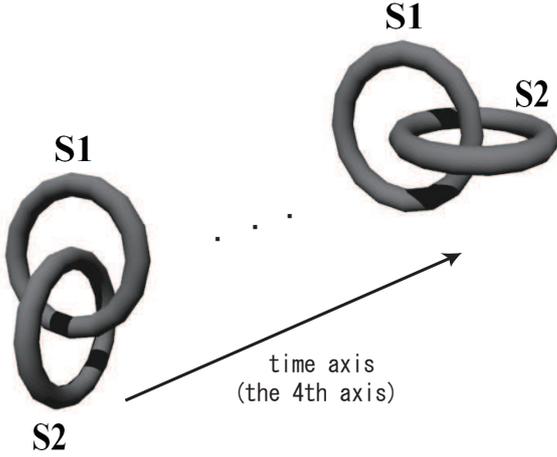


Fig. 1: Interlocking components in a four-dimensional tape.

(i_1, i_2, i_3, i_4) , as shown in Fig. 2. Furthermore, we define

$$x[(i_1, i_2, i_3, i_4), (i'_1, i'_2, i'_3, i'_4)],$$

when $1 \leq i_j \leq i'_j \leq l_j(x)$ for each integer $j(1 \leq j \leq 4)$, as the four-dimensional tape y satisfying the following :

- (i) for each $j(1 \leq j \leq 4)$, $l_j(y) = i'_j - i_j + 1$;
- (ii) for each r_1, r_2, r_3, r_4 ($1 \leq r_1 \leq l_1(y)$, $1 \leq r_2 \leq l_2(y)$, $1 \leq r_3 \leq l_3(y)$, $1 \leq r_4 \leq l_4(y)$), $y(r_1, r_2, r_3, r_4) = x(r_1 + i_1 - 1, r_2 + i_2 - 1, r_3 + i_3 - 1, r_4 + i_4 - 1)$.

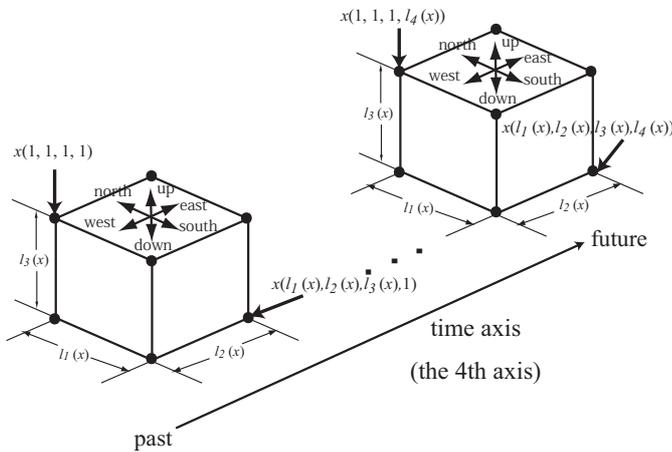


Fig. 2: Four-dimensional input tape.

Definition 2.2. A four-dimensional nondeterministic one-marker automaton $4-NM_1$ is defined by the six-tuple

$$M = (Q, q_0, F, \Sigma, \{+, -\}, \delta),$$

where

- (1) Q is a finite set of states;
- (2) $q_0 \in Q$ is the initial state;
- (3) $F \subseteq Q$ is the set of accepting states;
- (4) Σ is a finite input alphabet ($\# \notin \Sigma$ is the boundary symbol);
- (5) $\{+, -\}$ is the pair of signs of presence and absence of the marker; and
- (6) $\delta: (Q \times \{+, -\}) \times ((\Sigma \cup \{\#\}) \times \{+, -\}) \rightarrow 2^{(Q \times \{+, -\}) \times ((\Sigma \cup \{\#\}) \times \{+, -\}) \times \{\text{east, west, south, north, up, down, future, past, no move}\}}$ is the next-move function, satisfying the following: For any $q, q' \in Q$, any $a, a' \in \Sigma$, any $u, u', v, v' \in \{+, -\}$, and any $d \in \{\text{east, west, south, north, up, down, future, past, no move}\}$, if $((q', u'), (a', v'), d) \in \delta((q, u), (a, v))$ then $a = a'$, and $(u, v, u', v') \in \{(+, -, +, -), (+, -, -, +), (-, +, -, +), (-, +, +, -), (-, -, -, -)\}$.

We call a pair (q, u) in $Q \times \{+, -\}$ an extended state, representing the situation that M holds or does not hold the marker in the finite control according to the sign $u = +$ or $u = -$, respectively. A pair (a, v) in $\Sigma \times \{+, -\}$ represents an input tape cell on which the marker exists or does not exist according to the sign $v = +$ or $v = -$, respectively.

Therefore, the restrictions on δ above imply the following conditions. (A) When holding the marker, M can put it down or keep on holding. (B) When not holding the marker, and (i) if the marker exists on the current cell, M can pick it up or leave it there, or (ii) if the marker does not exist on the current cell, M cannot create a new marker any more.

Definition 2.3. Let Σ be the input alphabet of $4-NM_1$ M . An extended input tape \tilde{x} of M is any four-dimensional tape over $\Sigma \times \{+, -\}$ such that

- (i) for each $j(1 \leq j \leq 4)$, $l_j(\tilde{x}) = l_j(x)$,
- (ii) for each $i_1(1 \leq i_1 \leq l_1(\tilde{x}))$, $i_2(1 \leq i_2 \leq l_2(\tilde{x}))$, $i_3(1 \leq i_3 \leq l_3(\tilde{x}))$, and $i_4(1 \leq i_4 \leq l_4(\tilde{x}))$, $\tilde{x}(i_1, i_2, i_3, i_4) = x(i_1, i_2, i_3, i_4, u)$ for some $u \in \{+, -\}$.

Definition 2.4. A configuration of $4-NM_1$ $M = (Q, q_0, F, \Sigma, \delta)$ is an element of

$$((\Sigma \cup \{\#\}) \times \{+, -\})^{(4)} \times (Q \times \{+, -\}) \times N^4,$$

where N denotes the set of all nonnegative integers. The first component of a configuration $c = (\tilde{x}, (q, u), (i_1, i_2, i_3, i_4))$ represents the extended input tape of M . The second component (q, u) of c represents the extended state. The third component

(i_1, i_2, i_3, i_4) of c represents the input head position. If q is the state associated with configuration c , then c is said to be an *accepting configuration* if q is an accepting state. The *initial configuration* of M on input x is

$$I_M(x) = (x^-, (q_0, +), (1, 1, 1, 1)),$$

where x^- is the special extended input tape of M such that $x^-(i_1, i_2, i_3, i_4) = (x(i_1, i_2, i_3, i_4), -)$ for each i_1, i_2, i_3, i_4 ($1 \leq i_1 \leq l_1(\tilde{x}), 1 \leq i_2 \leq l_2(\tilde{x}), 1 \leq i_3 \leq l_3(\tilde{x}), 1 \leq i_4 \leq l_4(\tilde{x})$). If M moves determinately, we call M a *four-dimensional deterministic one-marker automaton 4-DM₁*.

Definition 2.5. A *seven-way four-dimensional Turing machine* is defined by the six-tuple

$$M = (Q, q_0, F, \Sigma, \Gamma, \delta),$$

where

- (1) Q is a finite set of *states*;
- (2) $q_0 \in Q$ is the *initial state*;
- (3) $F \subseteq Q$ is the set of *accepting states*;
- (4) Σ is a finite *input alphabet* ($\# \notin \Sigma$ is the *boundary symbol*);
- (5) Γ is a finite *storage-tape alphabet* ($B \in \Gamma$ is the *blank symbol*); and
- (6) $\delta \subseteq (Q \times (\Sigma \cup \{\#\}) \times \Gamma) \times (Q \times (\Gamma - \{B\}) \times \{\text{east, west, south, north, up, down, future, no move}\} \times \{\text{right, left, no move}\})$.

If M moves determinately (nondeterminately), we call M a *seven-way four-dimensional deterministic (nondeterministic) Turing machine SV4-DTM (SV4-NTM)*.

Let $L: \mathbf{N} \rightarrow \mathbf{R}$ be a function. A seven-way four-dimensional Turing machine M is said to be $L(m)$ *space bounded* if for all $m \geq 1$ and for each x with $l_1(x) = l_2(x) = l_3(x) = l_4(x) = m$, if x is accepted by M , then there is an *accepting computation path* of M on x in which M uses no more than $L(m)$ cells of the storage tape. We denote an $L(m)$ space-bounded SV4-DTM (SV4-NTM) by $SV4-DTM(L(m))$ ($SV4-NTM(L(m))$).

Definition 2.6. Let $T(M)$ be the set of four-dimensional tapes accepted by a machine M , and let $\mathcal{L}[4-DM_1] = \{T | T(M) \text{ for some } 4-DM_1 M\}$. $\mathcal{L}[4-NM_1]$, etc. are defined in the same way as $\mathcal{L}[4-DM_1]$.

We can easily derive the following theorem by using ordinary technique[6].

Theorem 2.1. For any function $L(m) \geq \log m^3$, $\mathcal{L}[SV4-NTM(L(m))] \subseteq U_{c>0} \mathcal{L}[SV4-DTM(2^{c(L(m))})]$.

3 Simulation of four-dimensional one-marker automata by four-dimensional Turing machines

In this section, we show the algorithms described in the previous section are optimal in some sense. We can get the following Theorems any using the same technique as in the proof of Lemmas 6.2 and 6.3 in [6].

Theorem 3.1. To simulate 4-DM₁'s, (1) SV4-NTM's require $\Omega(m^3 \log(m^3))$ space and (2) SV4-DTM's require $2^{\Omega(m^3 \log(m^3))}$ space ($m \geq 1$).

Theorem 3.2. To simulate 4-NM₁'s,

- (1) SV4-NTM's require $\Omega(m^6)$ space, and
- (2) SV4-DTM's require $2^{\Omega(m^6)}$ space ($m \geq 1$).

4 Recognizability of interlocking components in four-dimensional images

In this paper, we show that interlocking components are not recognized by any space-bounded four-dimensional Turing machines.

First of all, we consider a four-dimensional input tape T_3 that is 7 units in thickness. So, for some m , $T_3 = \{(i_1, i_2, i_3, i_4) \mid 1 \leq i_1, i_2, i_3 \leq m+2, 1 \leq i_4 \leq 7\}$. Fig.3(a) represents T_3 . Now we define two different $5 \times 5 \times 5$ patterns as shown in Fig.3(b)(c). Then we consider an arbitrary n -by- n matrix of those $5 \times 5 \times 5$ patterns (see Fig.3).

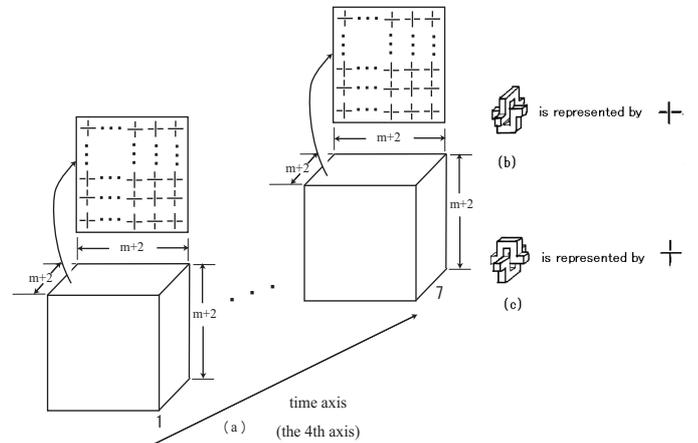


Fig. 3: Four-dimensional input tape including interlocking components T_3 [2].

Then, we can get the following lemma from Lemma 2.1 in [2].

Lemma 4.1. *4-DM, cannot recognize interlocking components of an arbitrary given digital picture.*

From Theorem 3.1 and Lemma 4.1, we can get the following.

Theorem 4.1. *Interlocking components are not accepted by any SV4-DTM ($L(m)$)(SV4-NTM($L(m)$)) for any function $L(m)$ such that $\lim_{m \rightarrow \infty} [L(m)/2^{m^3 \log m^3}] = 0$ ($\lim_{m \rightarrow \infty} [L(m)/m^3 \log m^3] = 0$)($m \geq 1$).*

Next, we can get the following lemma by using a technique similar to that in the proof of Lemma 2.1 in [2].

Lemma 4.2. *4-ND₁ cannot recognize interlocking components of an arbitrary given digital picture.*

From Theorem 3.2 and Lemma 4.2, we can get the following.

Theorem 4.2. *Interlocking components are not accepted by any SV4-DTM($L(m)$)(SV4-NTM($L(m)$)) for any function $L(m)$ such that $\lim_{m \rightarrow \infty} [L(m)/2^{m^6}] = 0$ ($\lim_{m \rightarrow \infty} [L(m)/m^6] = 0$)($m \geq 1$).*

5 Conclusion

In this paper, we dealt with recognizability of topological components by four-dimensional automata, and showed that interlocking components are not recognized by any space-bounded four-dimensional deterministic or nondeterministic Turing machines. By the way, what is the situation for a two or three marker automata, or for alternation (see [1])? This question seems very interesting. We will investigate the problem in further papers.

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